

XFP-10G31-LR

10Gbps XFP Transceiver, Single Mode, 10km Reach



Product Features

- Supports 8.0Gb/s to 11.1Gb/s bit rates
- Hot-pluggable XFP footprint, Built-in digital diagnostic
- ❖ Maximum link length of 10km with SMF
- ❖ 1310nm DFB laser and PIN photodiode
- XFP MSA package with duplex LC connector
- No reference clock required
- Single +3.3V power supply
- Power dissipation <1.5W</p>

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- Compatible with RoHS
- ❖ Temperature range: 0 to +70° C

Applications

- SONET OC-192&SDH STM-64 at 9.953Gbps
- 10GBASE-LR/LW 10G Ethernet
- ❖ 1200-SM-LL-L 10G Fibre Channel
- 10GE over G.709 at 11.09Gbps
- OC192 over FEC at 10.709Gbps
- Other optical links, up to 11.1Gbps

Description

The XFP module is compliant with the 10G Small Form-Factor Pluggable (XFP) Multi-Source Agreement (MSA), supporting data-rate of 8.0~11.1Gbps, and transmission distance up to 10km on SMF.

The transceiver module comprises a transmitter with 1310nm DFB laser and a receiver with a PIN photodiode. Transmitter and receiver are separate within a wide temperature range and offers optimum heat dissipation and excellent electromagnetic shielding thus enabling high port densities for 10 GbE systems.

Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply Voltage	Vcc	-0.5	4.5	V
Storage Temperature	Ts	-40	+85	°C
Operating Humidity	-	5	85	%

Any stress beyond the maximum ratings can result in permanent damage. The device specifications are guaranteed only under the recommended operating conditions.

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Recommended Operating Conditions

Parameter	Symbol	Min	Typical	Max	Unit
Operating Case Temperature	Tc	0		+70	°C
Power Supply Voltage @3.3V	Vcc	3.135	3.30	3.465	V
Power Supply Current	Icc			500	mA
Data Rate		8.0		11.1	Gbps

Optical and Electrical Characteristics

Parameter		Symbol	Min	Typical	Max	Unit	Notes
	Transmitter						
Centre Wa	velength	λς	1270	1310	1350	nm	
Spectral Widt	h (-20dB)	Δλ			1	nm	
Side-Mode Sup	pression Ratio	SMSR	30			dB	
Average Ou	tput Power	Pout	-6.5		+0.5	dBm	1
Extinctio	Extinction Ratio		3.5			dB	
Data Input Swi	ng Differential	VIN	180		950	mV	2
Input Differenti	al Impedance	ZIN	90	100	110	Ω	
TX Disable	Disable		2.0		Vcc	V	
1 X Disable	Enable		0		0.8	V	
			Receive	r			
Centre Wa	velength	λς	1260		1600	nm	
Receiver S	Sensitivity				-14.5	dBm	3
Receiver (Overload		0.5			dBm	3
LOS De-	LOS De-Assert				-15.5	dBm	
LOS Assert		LOSA	-26			dBm	
LOS Hysteresis			0.5		4	dB	
Data Output Swing Differential		Vout	400	600	800	mV	2
LOS		High	2.0		Vcc	V	
		Low			0.8	V	



Notes:

- 1. The optical power is launched into SMF.
- 2. Internally AC-coupled.
- 3. Measured with a PRBS 231-1 test pattern @9953Mbps, BER ≤1×10-12.

Pin Descriptions

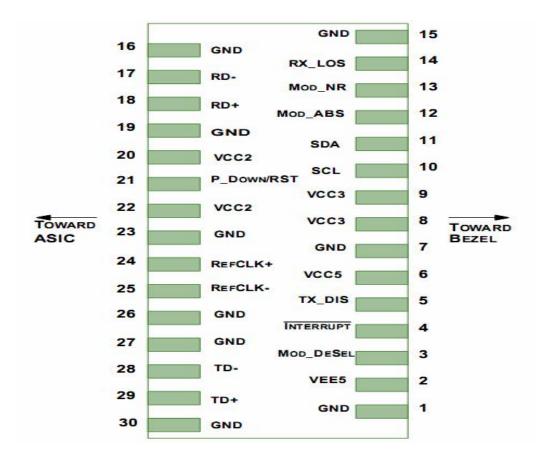
Pin	Logic	Symbol	Name/Description	Note
1		GND	Module Ground	1
2		VEE5	Optional –5.2 Power Supply – Not required	
3	LVTTL-I	Mod- Desel	Module De-select; When held low allows the module to, respond to 2-wire serial interface commands	
4	LVTTL-O	Interrupt	Interrupt (bar); Indicates presence of an important condition which can be read over the serial 2-wire interface	2
5	LVTTL-I	TX_DIS	Transmitter Disable; Transmitter laser source turned off	
6		VCC5	+5 Power Supply – Not required	
7		GND	Module Ground	1
8		VCC3	+3.3V Power Supply	
9		VCC3	+3.3V Power Supply	
10	LVTTL-I	SCL	Serial 2-wire interface clock	2
11	LVTTL- I/O	SDA	Serial 2-wire interface data line	2
12	LVTTL-O	Mod_Abs	Module Absent; Indicates module is not present. Grounded in the module.	2
13	LVTTL-O	Mod_NR	Module Not Ready;	2
14	LVTTL-O	RX_LOS	Receiver Loss of Signal indicator	2
15		GND	Module Ground	1
16		GND	Module Ground	1
17	CML-O	RD-	Receiver inverted data output	
18	CML-O	RD+	Receiver non-inverted data output	
19		GND	Module Ground	1
20		VCC2	+1.8V Power Supply – Not required	
24	21 LVTTL-I P_Down/R ST	Power Down; When high, places the module in the low power stand-by mode and on the falling edge of P_Down initiates a module reset		
21		ST	ST	Reset; The falling edge initiates a complete reset of the module including the 2-wire serial interface, equivalent to a power cycle.
22		VCC2	+1.8V Power Supply – Not required	
23		GND	Module Ground	1



24	PECL-I	RefCLK+	Reference Clock non-inverted input, AC coupled on the host board – Not required	3
25	PECL-I	RefCLK-	Reference Clock inverted input, AC coupled on the host board – Not required	3
26		GND	Module Ground	1
27		GND	Module Ground	1
28	CML-I	TD-	Transmitter inverted data input	
29	CML-I	TD+	Transmitter non-inverted data input	
30		GND	Module Ground	1

Notes:

- 1. Module circuit ground is isolated from module chassis ground within the module.
- 2. Open collector, should be pulled up with 4.7k 10k ohms on host board to a voltage between 3.15V and 3.6V.
- 3. A Reference Clock input is not required.



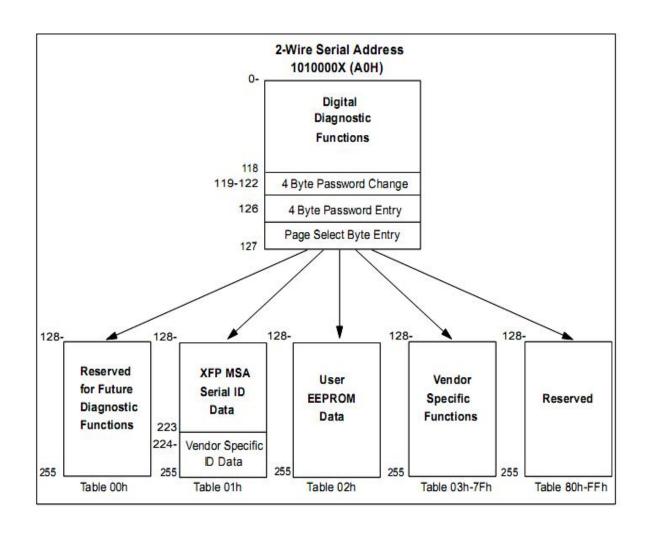


Management Interface

The transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA).

The Module provides diagnostic information about the present operating conditions. The transceiver generates this diagnostic data by digitization of internal analog signals. Alarm/warning threshold data is written during device manufacture. Received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring all are implemented.

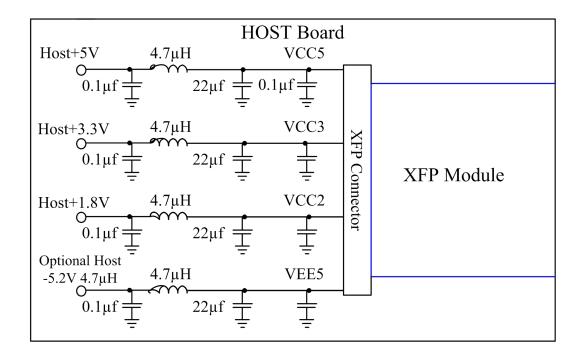
The digital diagnostic memory map specific data field defines as following.



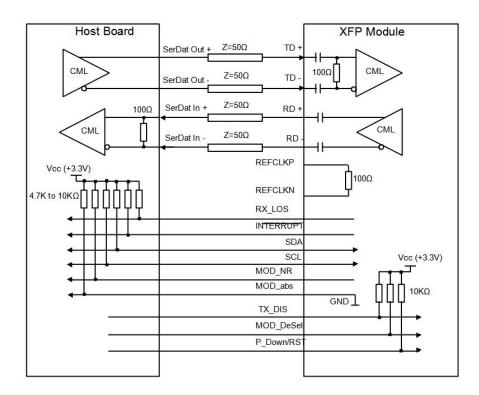
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Recommended Host Board Power Supply Circuit



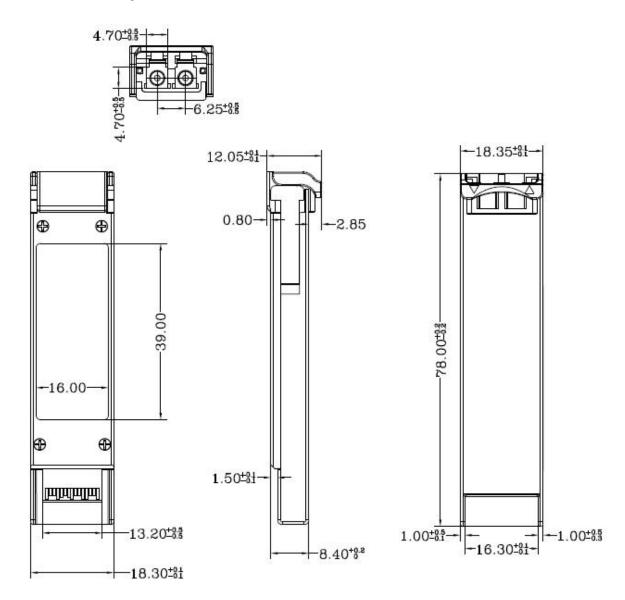
Recommended High-speed Interface Circuit



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Mechanical Specifications



Ordering Information

Part Number	Product Description
XFP-10G31-LR	8.0~11.1Gbps, XFP, 1310nm, 10km, 0°C ~ +70°C

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